

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled INTEGRATED MEMORY MAPPED CONTROLLER CIRCUIT FOR FIBER OPTICS TRANSCEIVER, the specification of which

(check one)	XX_ is atta	ched hereto.			
		ation Serial No. samended on	applicable)	_ as 	
above-identif		reviewed and und ion, including t			
examination o		isclose informat tion in accordan a).			
Code, §119 of certificate l application f	any foreign a isted below an or patent or i	rity benefits un pplication(s) fo d have also iden nventor's certif ion on which pri	r patent or tified below icate having	inventor' v any fore y a filing	s eign
Prior Foreign	Application(s)		Priority	Claimed
(Number)	(Country)	(Day/Month/Yea	ar Filed)	Yes	No
(Number)	(Country)	(Day/Month/Yea	r Filed)	Yes	No
United States matter of eac prior United paragraph of disclose mate Regulation, §	application(s h of the claim States applica Title 35, Unit rial informati 1.56(a) which	under Title 35,) listed below a s of this applic tion in the mann ed States Code, on as defined in occurred between l or PCT interna	nd, insofar ation is not er provided §112, I ackr Title 37, O the filing	as the su disclose by the fi nowledge t Code of Fe date of t	abject d in the rst he duty to deral he prior
(Application	Serial No.)	(Filing Date)	(patented,	•	atus) abandoned
(Application	Serial No.)	(Filing Date)	(patented,		atus) abandoned
(Application	Serial No.)	(Filing Date)		(S	tatus)

9775-0047-999

patented, pending, abandoned)

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File No. 9775-0052-999

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18, United States Code, §1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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